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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10073132	FILING DATE 02/13/2002	CLASS 708	SUBCLASS 53	GAU 2121	EXAMINER ANH TRAN
**APPLICANTS: Yoshida Shuji; Miura Daisuke; Arakawa Toshio; Nagasaka Mitsuki; Yoshida Kenji; Honda Hiroyuki; Kobayashi Kenji; Okamoto Masayuki; 326 2819					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-116174 04/13/2001 AT					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		107346-00020	
Verified and Acknowledged Examiners's initials					
TITLE : Logic circuit for fast carry/borrow					
U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
Assistant Examiner		DRAWING	
ISSUE FEE		Sheeto Drwg.	Fig. Drwg.
Amount Due	Date Paid	Print Fig.	
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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